Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.070”**

**ANODE**

**.059 X .059”**

**.070”**

**Top Material: Al**

**Backside Material: Au**

**Bond Pad Size: .059” X .059”**

**Backside Potential: CATHODE**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .070” X .070” DATE: 11/10/21**

**MFG: MICROSEMI THICKNESS .010” P/N: 1N5811**

**DG 10.1.2**

#### Rev B, 7/1